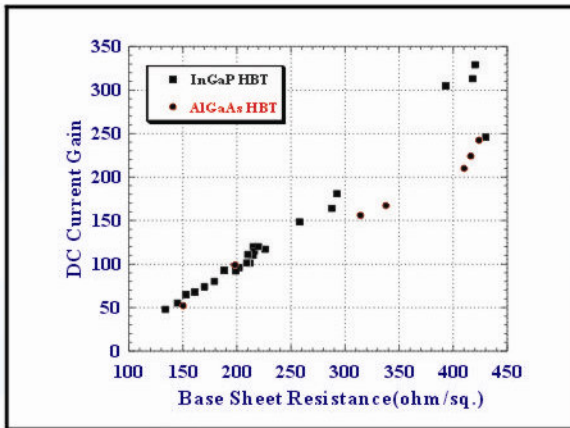


# VPEC Low Burn-in GaAs HBT Epi-Wafer

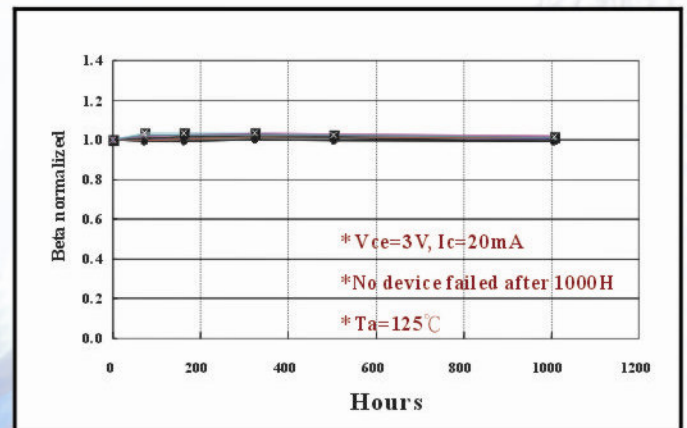
## Features:

1. Excellent device performance uniformity (STD < 3%) and consistency.
2. Very low burn-in effect (<5%): better reliability and save die sort testing time.
3. Wide range tunable DC performance:  $\beta / R_{bs} = 0.3 \sim 0.7$
4. Long term mass production experience.
5. Excellent device reliability and high ruggedness.
6. Provide epi growth DOE to accelerate customers' R&D projects.
7. Cost effective.

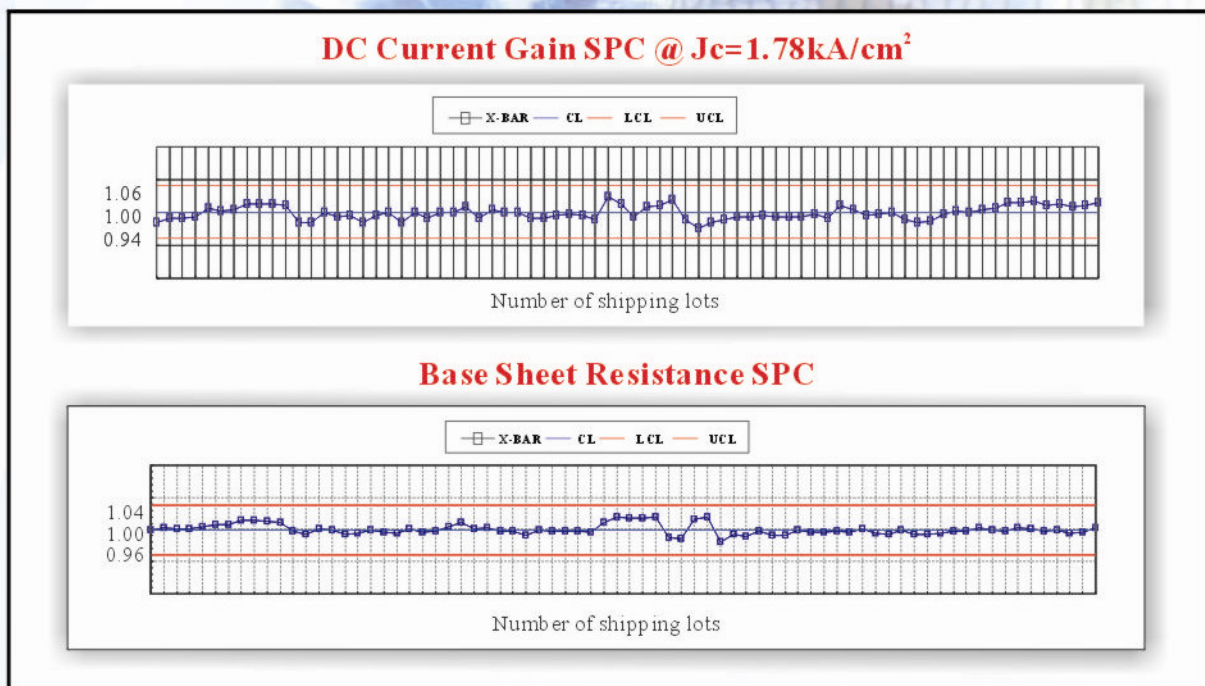
## Performance of VPEC Low Burn-in HBT Wafer



## HBT Device Reliability



## One of Production GaAs HBT Wafer SPC



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